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B Archambeault, AE Ruehli - Electromagnetic Compatibility, IEEE Transactions on, 2001 - ieeexplore.ieee.org

... and TA Schreyer, "Computation of switching noise in printed circuit boards," IEEE ... for the partial element equivalent circuit (PEEC) method," presented ...

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Estimating the noise mitigation effect of local decoupling in printed circuit boards - all 5 versions »

J Fan, W Cui, JL Drewniak, TP Van Doren, JL ... - Advanced Packaging, IEEE Transactions on [see also ..., 2002 - ieeexplore.ieee.org

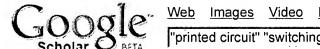
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An efficient simultaneous switching noise analysis of high densitymulti-layer packages and PCBs ...

JH Choi, YS Hong, CW Ko, YG Kim, TS Kim, JT Kong - VLSI and CAD, 1999. ICVC'99. 6th International Conference on, 1999 - ieeexplore.ieee.org

... 10 hours using the conventional PEEC approach. ... Speed Digital Packages and Printed Circuit Boards," 35th ... SH Lee, "A Simultaneous Switching Noise Analysis System ... Cited by 4 - Related Articles - Web Search



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J Yook

J Drewniak

T Hubing

Computation of switching noise in printed circuit boards

JG Yook, V Chandramouli, LPB Katehi, KA Sakallah, ... - Components, Packaging, and Manufacturing Technology, Part A, ..., 1997 - ieeexplore ieee.org

... 20, NO. 1, MARCH 1997 Computation of Switching Noise in Printed Circuit Boards Jong-Gwan Yook, Student Member, IEEE, V. Chandramouli, Student Member, IEEE, ... Cited by 30 - Related Articles - Web Search - BL Direct

Power bus decoupling on multilayer printed circuit boards - all 3 versions »

TH Hubing, JL Drewniak, TP Van Doren, DM Hockanson - Electromagnetic Compatibility, IEEE Transactions on, 1995 - ieeexplore.ieee.org

... 19, A simple finite-difference frequency-domain (FDFD) algorithm for analysis of switching noise in printed circuit boards and packages, Ramahi, OM; Subramanian ... Cited by 72 - Related Articles - Web Search - BL Direct

A novel power plane with integrated simultaneous switching noise mitigation capability using high ... - all 10 versions »

T Kamgaing, OM Ramahi - Microwave and Wireless Components Letters, IEEE [see also 2003 - ieeexplore.ieee.org

... In printed circuit boards (PCB), each digital gate is usually connected between two ... 1. The simultaneous switching noise (SSN) becomes acute when the noise ... Cited by 48 - Related Articles - Web Search - BL Direct

Reducing simultaneous switching noise and EMI on ground/powerplanes by dissipative edge termination - all 7 versions »

I Novak, SM Inc. MA Burlington - Advanced Packaging, IEEE Transactions on [see also 1999 - ieeexplore.ieee.org

... [17] P. Wetmore, F. Chua, and X. Yuan, "Modeling simultaneous switching noise in high speed multilayer packages and printed circuit boards," in Proc. ...

Cited by 43 - Related Articles - Web Search - BL Direct

Modeling of simultaneous switching noise in high speed systems - all 6

S Chun, M Swaminathan, LD Smith, J Srinivasan, Z ... - Advanced Packaging, IEEE Transactions on [see also ..., 2001 - ieeexplore.ieee.org

... CHUN et al.: MODELING OF SIMULTANEOUS SWITCHING NOISE IN HIGH SPEED SYSTEMS ... been

used to model a system containing packages on a printed circuit board, demon ... Cited by 48 - Related Articles - Web Search - BL Direct

Interconnect and circuit modeling techniques for full-chip powersupply noise analysis - all 2 versions »

HH Chen, JS Neely - Components, Packaging, and Manufacturing Technology, Part B: ..., 1998 - ieeexplore ieee org

... and achieve the desired resolution for a full-chip switching noise analysis, we use ... and the bottom of the package is attached to the printed circuit board (PCB ... Cited by 69 - Related Articles - Web Search - BL Direct

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Proceedings of the IEEE

Volume 89, Issue 5, May 2001 Page(s):740 - 771

Digital Object Identifier 10.1109/5.929652

AbstractPlus | References | Full Text: PDF(404 KB) | Full Text: HTML | IEEE JNL

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 Modeling DC power-bus structures with vertical discontinuities using a circuit extraction ap mixed-potential integral equation

Jun Fan; Hao Shi; Orlandi, A.; Knighten, J.L.; Drewniak, J.L.;

Advanced Packaging, IEEE Transactions on [see also Components, Packaging and Manufacturing

Advanced Packaging, IEEE Transactions on]

Volume 24, Issue 2, May 2001 Page(s):143 - 157

Digital Object Identifier 10.1109/6040.928748

AbstractPlus | References | Full Text: PDF(376 KB) | IEEE JNL

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5. Modeling and simulation of core switching noise for ASICs

Nanju Na; Jinwoo Choi; Swaminathan, M.; Libous, J.R.; O'Connor, D.P.;

Advanced Packaging, IEEE Transactions on [see also Components, Packaging and Manufacturing

Advanced Packaging, IEEE Transactions on)

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Γ 5. Capacitor placement using genetic algorithms to reduce switching noise Karaki, S.H.; Kayssi, A.I.; Abdouni, B.; Raad, M.-C.; Microelectronics, The 14th International Conference on 2002 - ICM